

Form 1449*	Atty. Docket No.: 303.451US6	Serial No. 09/745,780
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Martin Ceredig Roberts et al.	
	Filing Date: December 21, 2000	Group: 2823

U. S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<u>NB</u>	5,506,172	04/09/1996	Tang, S.	437	189	08/29/94

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
<u>NB</u>	Wolf, S.,	In: Silicon Processing for the VLSI Era -- Vol. 1: Process Technology,	Lattice Press, Sunset Beach, CA, 175-177,	(1986)		

Examiner <u>Neal Bererny</u>	Date Considered <u>5/20/02</u>
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*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.